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Answer : Local skew : The difference between the clock reaching at the launching flop vs the clock reaching the destination flip-flop of a timing-path. Global skew : The difference between the earliest reaching flip-flop and latest reaching flip-flop for a same clock-domain.

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second state is the current state. One-Cold encoding: Same as one-hot encoding except that 0 is the valid value. If there are four states then it requires four bits (four flip-flops) to represent the current state.

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micro processor, memory and I/O signal lines are fabricated using VLSI technology. 27. What is the priority order of hard ware interrupts in 8085 ? Trap, RST 7.5, RST 6.5, RST 5.5 and INTR. Trap has the highest priority and INTR has the lowest.

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